

# Snap On Board eUSB 3.1 FMC

eUSB 3.1 FMC (FPGA Mezzanine Card) makes it easier to validate USB 3.1 functionality with Intel FPGA having in-built serial transceivers. It also contains ULPI PHY chip to validate USB 2.0 functionality.

## Features

- Standard Board Interface
  - FMC
- USB Interface
  - x3 USB Type C Connectors
  - x2 USB Power switches to facilitate USB Host mode evaluation
- User Interface
  - x4 user defined LEDs
- Jumpers
  - For configuring TUSB1002 (USB 3.1 Linear re-driver and signal conditioner)
- Clock
  - 25 MHz clock oscillator with clock generator IC for USB 3.1 interface
- Easy to Use
  - Makes it easy to evaluate various SLS's USB IP cores

## Deliverables

- eUSB 3.1 FMC
- USB 3.1 Type C Cable
- Evaluation Demo for Intel® Arria®10 GX Development Kit

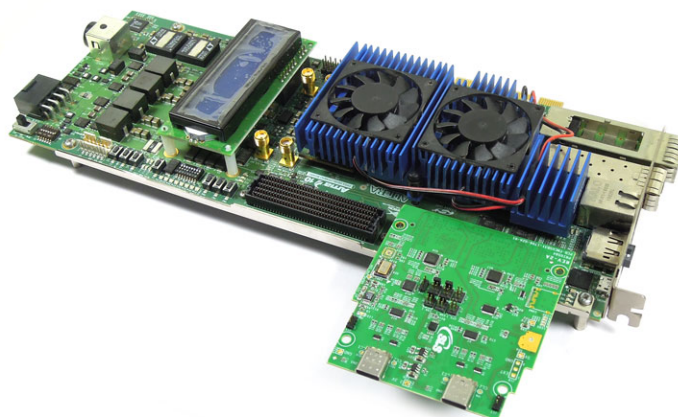
**Note:** The board does not include any reference design.

## Application

- Evaluation of USB IP cores

## Application Examples

The following example shows the eUSB 3.1 FMC connection with Intel® Arria®10 GX Development Kit.



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